

# Dordt University Engineering Department

## EGR 323, Electronics I

Spring 2021 Syllabus

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| <b>2020-21 Catalog Data:</b>           | <b>EGR 323 Electronics II (4 credit hours)</b> (Spring, odd calendar years)<br>A continuation of Engineering 322. Topics include biasing strategies for discrete and integrated circuit designs, current mirrors, differential and multistage amplifiers, frequency response, feedback, and stability. The laboratory includes construction of a kit, which introduces students to power output stages, tuned amplifiers, and demodulator circuits. The laboratory also includes a short design problem. Prerequisite: Engineering 322.  |
| <b>Textbook:</b>                       | Sedra, Smith, Carusone, and Gaudet, <i>Microelectronic Circuits</i> , 8th ed., Oxford University Press, 2020. ISBN 978-0-19-085346-4 hardcover version; ISBN 978-0-19-085353-2 e-Book version; ISBN 978-0-19-085-549 looseleaf version.  |
| <b>References:</b>                     | Horowitz and Hill, <i>The Art of Electronics</i> , 3rd ed., Cambridge University Press.<br>Tuinenga, Paul W., <i>SPICE: A Guide to Circuit Analysis and Simulation Using Pspice</i> , 3rd edition, Prentice Hall, 1995.  |
| <b>Instructor:</b>                     | Professor Douglas De Boer, office location SB2608, office telephone 712-722-6245, e-mail: Douglas.DeBoer@Dordt.edu Prof. Office hours: virtual open office policy.   |
| <b>Course Objectives and Outcomes:</b> | <i>Creational Structure, fundamentals (SO1):</i> Students will be able to model and analyze several types of circuits that involve two or more transistors. Typical examples are current mirrors, differential pairs, cascode pairs, etc. This is the main goal of this course. Additional goals are listed below.<br><i>Creational Development, Communication (SO3):</i> Students will be able to identify basic circuit strategies useful for reaching various goals such as high gain, wide bandwidth, etc.. They will understand tradeoffs involved in choosing these strategies. They will be able to express these tradeoffs in oral and written forms and will be able to effectively create graphical aids to communication. |
| <b>Prerequisites by topic:</b>         | Calculus including techniques of integration, sequences, and series. Differential Equations. Linear circuit analysis including network theorems, first and second-order circuits, concepts in AC circuits such as frequency and phase, sinusoidal steady-state analysis and phasors. Solid state device physics at the level of equations that model the behavior of diodes and transistors, methods of amplification with single-transistor circuits.   |
| <b>Laboratory:</b>                     | The laboratory session meets for three hours each week. See the schedule on the next page.   |
| <b>Computer use:</b>                   | Orcad-Pspice is supported for circuit simulation. Students are encouraged (but not required) to use programs such as Matlab and/or Desmos for homework solutions when appropriate, especially for making graphs. Xcircuit will be introduced for drawing schematics. All assignments and handouts are available via Dordt's "Canvas" course management system, <a href="https://dordt.instructure.com">https://dordt.instructure.com</a> . An NCEES-approved calculator is recommended for all homework, tests and the final exam. See <a href="https://ncees.org/exams/calculator/">https://ncees.org/exams/calculator/</a>   |
| <b>Academic Integrity:</b>             | This course is subject to the College's policies on academic integrity. ( <a href="https://www.dordt.edu/student-life/student-handbook/general-information#Academic%20Integrity">https://www.dordt.edu/student-life/student-handbook/general-information#Academic%20Integrity</a> .) Also see the standards of scholarship for this course at <a href="https://dfdeboer.github.io/integrity.htm">https://dfdeboer.github.io/integrity.htm</a> also posted on the course web page and see policies on the following pages of this syllabus.   |
| <b>Accommodations:</b>                 | Students who require assistance or accommodations based on the impact of a documented disability must contact the Coordinator of Services for Students with Disabilities to access accommodations. Contact Marliss Van Der Zwaag at the Academic Enrichment Center, Telephone 722-6490, e-mail <a href="mailto:Marliss.VanDerZwaag@dordt.edu">Marliss.VanDerZwaag@dordt.edu</a>  |
| <b>Means of Evaluation:</b>            | Homework will be due on a weekly basis (10%), Two Tests (25% each), Formal Laboratory Report(s) due near the end of semester (10%), Lab radio project progress (5%), Final Exam (25%) Professor De Boer records grades using grade points, A = 4.00, A- = 3.67, B+ = 3.33, B = 3.00, . . . D- = 0.67, F = 0.00. See <a href="https://dfdeboer.github.io/GDS.HTM">https://dfdeboer.github.io/GDS.HTM</a> for full details.  |
| <b>Role of this Course</b>             | This course is taught at the junior/senior level. It is intended for engineering and computer science majors. It is required for engineering majors who select the electrical concentration.   |

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### Course Outline

| Dates   | Class   | Laboratory   |
|---|---|--|
|   | Meets MWF 12:00 to 12:50 PM in room SB2803  | Sec 02 Mon., 01 Wed.<br>2-5 PM in room SB2803            |
| 1/15  | Introduction: IC design issues, $1 + 1 = 3$ idea, FET and BJT current mirrors<br><i>Text: Chapter 8</i>   | (no lab this week)                                       |
| 1/18 1/20 1/22                                  | Intrinsic gain, cascode amplifier, Darlington, Sziklai pairs, class B and class AB amplifiers<br><i>Text: Chapters 8, portions of Chapter 12</i>            | Logic Gates  |
| 1/25 1/27 1/29                                  | Wilson, Widlar current mirrors and other variations<br><i>Text: Chapter 8</i>   | Class B and C amplifiers                                 |
| 2/01 2/03 2/05                                  | Differential amplifiers and common-mode rejection<br><i>Text: Chapters 9</i>  | AM/FM radio project, AF section                          |
| 2/08 2/10 2/12                                  | MOSFET and BJT differential amplifiers, active loads<br><i>Text: Chapter 9</i>  | AM/FM radio project, AM detector, IF                     |
| 2/17 2/19<br>(no class on 2/15—<br>Reading Day) | Second-order differential amplifier issues: DC offsets, slew rate, multistage differential amplifiers<br><i>Text: Chapter 9</i><br><b>Test #1 on Friday</b> | AM/FM radio project, AM mixer, RF section and alignment* |
| 2/22 2/24 2/26                                  | Review of sinusoidal steady-state analysis<br><i>Text: Appendix F (see also your EGR 220 text)</i>  | Amplifier design project, 1 <sup>st</sup> of 4 weeks*    |
| 3/01 3/03 3/05                                  | Poles, zeros, bode plots in magnitude and phase<br><i>Text: Appendix F (see also your EGR 220 text)</i>   | Amplifier design project, 2 <sup>nd</sup> of 4 weeks*    |
| 3/08 3/10 3/12                                  | High frequency models for MOSFETs and BJTs<br><i>Text: Chapter 10</i>   | Amplifier design project, 3 <sup>rd</sup> of 4 weeks*    |
| 3/15 3/17 3/19                                  | High frequency response of CS, CE amplifiers<br>Estimating the effects of non-dominant poles<br><i>Text: Chapter 10</i>                                     | Amplifier design project, 4 <sup>th</sup> of 4 weeks*    |
| 3/22 3/24 3/26                                  | High frequency response of CG, CB, Cascode amps<br><i>Text: Chapter 10</i>  | AM/FM radio project, FM detector and IF*                 |
| 3/29 3/31 4/02                                  | High frequency response of CD, CS and diff amps<br><i>Text: Chapter 10</i> <b>Test #2 on Wednesday</b>  | AM/FM radio project, FM mixer, RF*                       |
| 4/05<br>(no class 4/07, reg. day)               | 4/09<br>Feedback: Four configurations.<br><i>Text: Chapter 11</i>   | (No lab this week)                                       |
| 4/12 4/14 4/16                                  | Voltage feedback amplifiers, other configurations<br><i>Text Chapter 11</i> <b>Lab report due on Wednesday</b>  | AM/FM radio project, FM alignment                        |
| 4/19 4/21 4/23                                  | Feedback and stability<br><i>Text Chapter 11</i>  | Network analyzer demo, catch-up on AM/FM                 |
| 4/26 4/28                                       | Feedback and stability<br><i>Text Chapter 11</i>  | Prof will be present. Catch-up, consulting.              |
| Wednesday, 5/08                                 | <b>Final exam on Wednesday, 10:30 a.m. – 12:30 p.m.</b>   |  |

This schedule may vary up to two weeks in order to accommodate student interests and abilities.

\*The Monday lab section will do this lab in the week following that which is shown here.

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The information on this and the following pages is partially copied from the Student Handbook as per policy in Dordt College's "Syllabus Checklist." Additional information specific to this course (not in the Student Handbook) is in a serif typeface and has a black line down the left margin.

### Academic Integrity

Dordt College is committed to developing a community of Christian scholars where all members accept the responsibility of practicing personal and academic integrity in obedience to biblical teaching. For students, this means not lying, cheating, or stealing others' work to gain academic advantage; it also means opposing academic dishonesty.

Academic Dishonesty. Students found to be academically dishonest will receive academic sanctions from their professor (from a failing grade on the particular academic task to a failing grade in the course), who will report the incident and the sanction given to the Student Life Committee for possible institutional sanctions (from a warning to dismissal from the college).

Appeals in such matters will be handled by the student disciplinary process as outlined in the Student Handbook.

Definitions

**Academic dishonesty** at Dordt College includes, but is not limited to, the following behaviors:

**Stealing/Plagiarizing:** copying another's work or ideas and creating the impression that they are one's own by failing to give proper credit or citation. This includes reading or hearing another's work or ideas and using them as one's own; quoting, paraphrasing, or condensing another's work without giving proper credit; purchasing or receiving another's work and using, handling, or submitting it as one's own work.

**Cheating:** unauthorized use of any study aids, equipment, or another's work during an academic task. This includes using unauthorized aids or other equipment during an examination; copying or looking at another individual's examination; taking or passing information to another individual during or after an examination; taking an examination for another individual; allowing another individual to take one's examination; stealing examinations.

All graded academic tasks are expected to be performed on an individual basis unless otherwise stated by the instructor.

An academic task may not be submitted by a student for course credit in more than one course without the permission of all instructors.

**Lying/Fabricating:** the intentional, unauthorized falsification or invention of any information or citation during an academic task. This includes changing or adding an answer on an examination and resubmitting it to change the grade; inventing data for a laboratory exercise or report.

**Facilitating Academic Dishonesty:** knowingly allowing or helping another individual to plagiarize, cheat, or fabricate information.

Students must do their own work. In Professor De Boer's courses students may *verbally* discuss their work in progress, including, papers and reports and their lab work (apparatus and demonstrations thereof). But they may not show un-graded papers or ungraded reports to each other. Detail on this policy can be found on the web at <https://dfdeboer.github.io/integrity.htm#DYOW>

### Attendance

Students are expected to be present for every class and laboratory period. Penalties for absence from class are left to the instructor. No designated number of skips is permitted.

**Student Responsibility:** Students shall notify each professor concerning the reason for absence prior to or immediately upon returning to class or in accordance with the instructor's method of accounting for absences. Students shall notify student services concerning all illnesses.

**Unexcused absences** are defined as failing to notify the instructor of the reason for the absence, or if the instructor deems the reason as illegitimate.

**Faculty initiatives:** The instructor may contact student services to check on the illness record of the students. They should also alert student services and contact the student directly concerning excessive absences, and must, if asked, report attendance patterns. Any instructor may, after due warning and according to guidelines established in the class syllabus, penalize the student by reducing the semester grade by a given percentage.

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**Student Services Responsibility:** Normally, student services does not notify instructors concerning student illness. Student services may alert instructors to serious problems. Decisions to inform instructors about serious problems will be made balancing the need to respect confidentiality and the responsibility to keep instructors appropriately informed about their students. Any student with serious problems is strongly advised to work closely with student services and follow the process to insure adequate communication between all parties in as efficient a way as possible.

**Excused Absence for Activities:** Students have obligations in many realms, so special care shall be taken not to demand commitments for participation in extra-curricular events that cause neglect in other areas. Sponsors/coaches shall inform students from the beginning of the time and effort expected of them. Sponsors/coaches shall demand a minimum of absences from other classes, restrict student involvement to only those crucially involved, and make efforts to choose a time/date for the event that is least invasive of classroom or lab time. In the case of conflicts, resolution shall be the responsibility of the sponsor/coach and the instructor with no penalty to the student (The appeals process outlined in the section titled Complaints Regarding Instruction in the Student Handbook shall be used if needed). The sponsor shall email faculty and student services a list of names, dates, and activities in advance of the event. The student must contact the instructor and make arrangements for any missed work.

### Late work

Professor De Boer expects to be notified at least a day in advance when you can reasonably be expected to have known that far in advance of a time when you will have to miss a class for a scheduled event of higher priority. In addition to the options listed above, missing classes without notification or for insubstantial reasons could be cause for being classified as an "uncooperative student" which could lead to dismissal from the course.

Be coachable. Start work early so you can ask questions in class and at the Professor's office. Anything handed in late will be accepted for possible grading, but no grade will be entered in the grade book, the work will not be returned to you, and the empty grade will function as a zero or an "F." Usually the item will never be graded. If, in the judgment of Prof. De Boer, grading the late item might improve the course grade, and if the reasons for the late work are acceptable and if there is no pattern of carelessness, then Prof. De Boer may choose to estimate a grade or actually grade the late work and enter the grade(s) in the grade book. Prof. De Boer may make a decision to estimate or fully grade a late item at any time after the item is handed in, but usually will do so only at the end of the course after all student course activities are complete. Additionally, if a pattern of late work develops, Professor De Boer will warn the student. After that warning if the problem is not resolved, a reduced course grade might result and/or the student may be classified as "uncooperative" which could lead to dismissal from the course.

### Missed tests or exams

Professor De Boer announces his test schedule in the first week of classes. During the first two or three weeks of classes and possibly at other times, if there is good cause, students may negotiate to change the test date(s) for the entire class to avoid a conflict for any one student. However, in the week before a test Prof. De Boer is very reluctant to negotiate the date. If you realize that you have a schedule conflict with a test date, discuss this with Prof. De Boer as soon as possible.

If you are late to a test you must still finish at the scheduled time.

If you miss a test or exam entirely the test or exam will go in the grade book as a blank score which will count as an "F." At the end of the semester after all your course work is complete Prof. De Boer will reassess the situation and might choose to estimate what he thinks you might have earned on the test based on any evidence he can find relevant to the situation. If an estimated grade is granted, it may still be discounted to a lower grade than the other tests and exams you completed if negligence is a partial cause for missing the exam. A dead cell phone battery that causes you to miss an alarm is an example of negligence. If a test is missed due to illness (fever, nausea, etc., not just a "bad cold") then be sure to report the illness to student services before the test or during the test period or as early as is reasonable. If student services can verify your illness to Prof. De Boer, an estimated grade that is non-punitive will be given at the end of the semester.

### Class Participation

Professor De Boer does not grade class participation—it is expected. If your participation is a problem Prof. De Boer will talk about it privately with you. Lack of participation can be a cause for adjusting your course grade downward, even to an "F."

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### Description of assignments

**Homework:** Generally a homework assignment will be due once each week. Expect the assignment to take about six hours to finish. Expect to have to ask for help to finish it. Make use of office hours or e-mail, or make telephone calls to the professor to get help as you need it.

**Tests:** Two tests and a final exam will be given. The first test is closed-book, but a crib sheet is allowed. The other test and the final exam are open book and open computer.

**Lab Report:** The lab is a time for conversation and exploration and communication. Much of what is done there involves kinesthetic and judgmental skills in using the instrumentation well, laying out prototype circuits, and interpreting observations. Situations for each student vary tremendously depending on a multiplicity of factors. It is not sufficient to rely on grading as your feedback for learning in the lab. Therefore, ask questions and show your results to professor De Boer as things happen. Your grade will be better because you will learn more in the time available if you get your feedback in real time as you work. Be coachable. The reports will be graded for style, completeness, and accuracy.

**Lab progress:** A little over half of the lab time will be devoted to the assembly of a kit. This will be graded by assessing your completion of the assembly and alignment work assigned.