

7-B Re-draw Figure 7-6 (text page 347) at a lower level of hierarchy. Assume that  $n = 4$ . In Figure 7-6 Register R2 has no data or control inputs. Use value fixing in place of R2 so that the data from R2 is always  $1101_2$ . (Thus R2 has no gates, it is just wires connected to +5 V or ground.) Note figure 4-7 on page 160 of your text. Also note Figure 7-2 part (c) on page 339 of your text. Use D-type flip-flops for the single-bit registers V and C.