3-96 Illustrate how the additions given below can be done in an 8-bit ones’ complement

number system. Follow the procedure of setting up the problem, considering

the carry-in (no need to write this step out, you may do it “in your head.”), forming   
 the sum (maybe only partially), wrapping around the carry-out and finishing the sum,   
 and evaluating overflow. Mark each of your sums with “overflow” or “no overflow” as   
 the case may be.

(a)

(b)

(c)

(d)