ENGINEERING DEPT. Mano, Kime, Martin $5^{\text {th }}$
NAME
COURSE EGR 204 SUBJECT PS \#A PAGE 1 OF 1 DATE 10/10/2019
3-92 Use BCD addition in a two-stage process as demonstrated in class to perform the following decimal additions problems in 16-bit packed BCD words.
If overflow occurs, write down the resulting 16 -bit sum and follow that with the annotation, "overflow."
(a) $6492+105$
(b) $6492+115$
(c) $6492+185$
(d) $6492+4693$

